

**WHAT IS CLAIMED IS:**

1. For use with an integrated circuit (IC) having a testing  
2 port, a system for securing said IC as against subsequent  
3 reprogramming, comprising:

4 port inhibit circuitry located on said IC and modifiable to  
5 achieve a configuration that determines an extent to which said  
6 testing port is enabled; and

7 port access circuitry, coupled to said testing port, that  
8 enables said testing port based on said configuration.

2. The system as recited in Claim 1 wherein said testing  
2 port is a Joint Test Action Group (JTAG) port.

3. The system as recited in Claim 1 wherein said port  
2 inhibit circuitry comprises an inhibit bit in a one-time  
3 programmable register.

4. The system as recited in Claim 1 wherein said port  
2 inhibit circuitry is configured to be permanently modified prior to  
3 delivering said IC to a user thereof.

5. The system as recited in Claim 1 wherein said extent is  
2 selected from the group consisting of:

3 fully enabled,  
4 only partially disabled, and  
5 completely disabled.

6. The system as recited in Claim 1 wherein said testing  
2 port comprises a direct loopback between input and output pins  
3 thereof.

7. The system as recited in Claim 1 wherein said IC is a  
2 baseband chip of a mobile communication device.

8. For use with an integrated circuit (IC) having a testing  
2 port, a method of securing said IC as against subsequent  
3 reprogramming, comprising:

4 modifying port inhibit circuitry located on said IC to achieve  
5 a configuration that determines an extent to which said testing  
6 port is enabled; and

7 enabling said testing port based on said configuration.

9. The method as recited in Claim 8 wherein said testing  
2 port is a Joint Test Action Group (JTAG) port.

10. The method as recited in Claim 8 wherein said port  
2 inhibit circuitry comprises an inhibit bit in a one-time  
3 programmable register.

11. The method as recited in Claim 8 wherein said modifying  
2 comprises permanently modifying said port inhibit circuitry prior  
3 to delivering said IC to a user thereof.

12. The method as recited in Claim 8 wherein said extent is  
2 selected from the group consisting of:

3 fully enabled,

4 only partially disabled, and

5 completely disabled.

13. The method as recited in Claim 8 wherein said testing  
2 port comprises a direct loopback between input and output pins  
3 thereof.

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14. The method as recited in Claim 8 wherein said IC is a  
2 baseband chip of a mobile communication device.

15. An electronic device, comprising:

an integrated circuit (IC), including:

a testing port,

port inhibit circuitry located on said IC and modifiable to achieve a configuration that determines an extent to which said testing port is enabled, and

port access circuitry, coupled to said testing port, that enables said testing port based on said configuration.

16. The electronic device as recited in Claim 15 wherein said testing port is a Joint Test Action Group (JTAG) port.

17. The electronic device as recited in Claim 15 wherein said port inhibit circuitry comprises an inhibit bit in a one-time programmable register.

18. The electronic device as recited in Claim 15 wherein said port inhibit circuitry is configured to be permanently modified prior to delivering said IC to a user thereof.

19. The electronic device as recited in Claim 15 wherein said extent is selected from the group consisting of:

fully enabled,

only partially disabled, and

5           completely disabled.

20.   The electronic device as recited in Claim 15 wherein said  
2    electronic device is selected from the group consisting of:  
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3           a mobile telephone,  
4           a PDA,  
5           an MDA,  
6           an MP3 player, and  
7           a set-top box.